



CMOS Cascade Sigma-Delta Modulators for Sensors and Telecom: Error Analysis and Practical Design (Analog Circuits and Signal Processing)

Rocío Río Fernández, Fernando Medeiro Hidalgo, Belén Pérez-Verdú, José Manuel Rosa Utrera, Ángel Rodríguez-Vázquez

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Institutional book, not really for bookstore catalogue

The book contains valuable information structured to provide insight on how to design SC sigma-delta modulators. It presents architectures, circuits, models, methods and practical considerations for the design of high-performance low-pass switched-capacitor (SC) sigma-delta A/D interfaces for mixed-signal CMOS ASICs. The main focus of the book is on cascade architectures. It differs from other books in the complete, in-depth coverage of SC circuit errors.

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